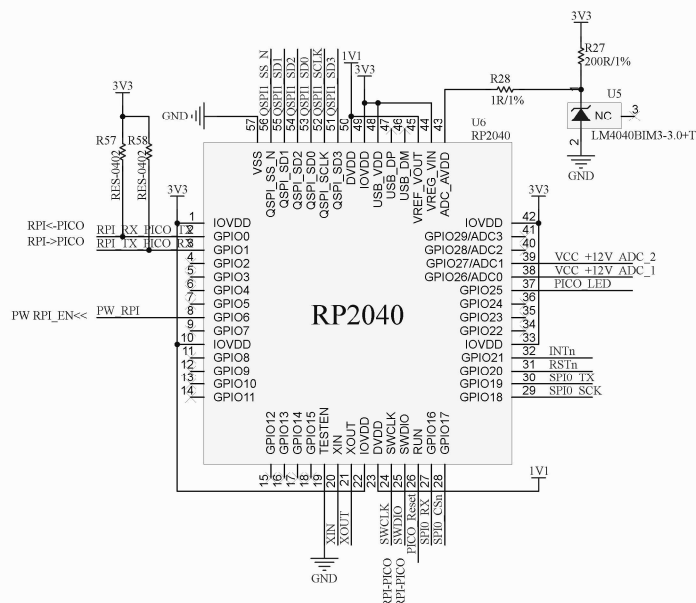
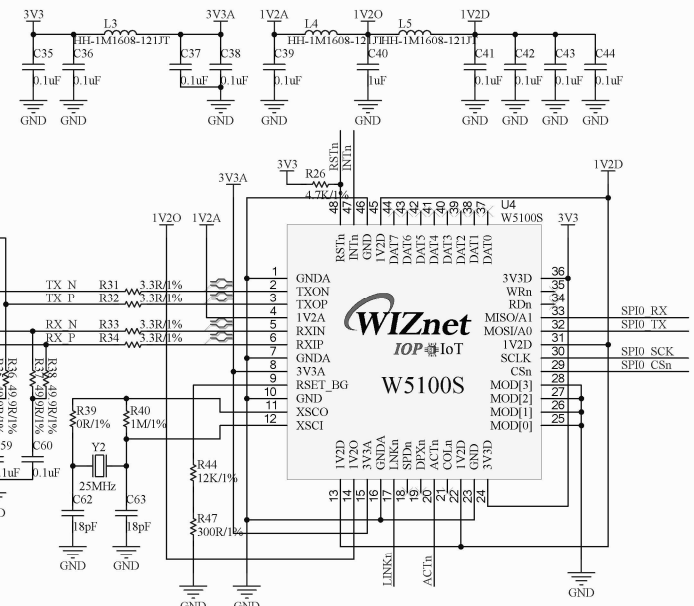
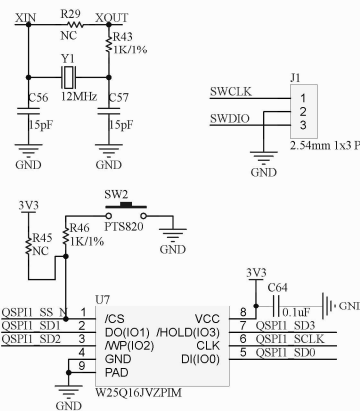
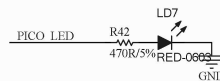
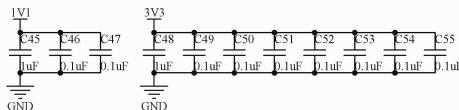
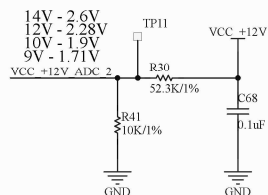
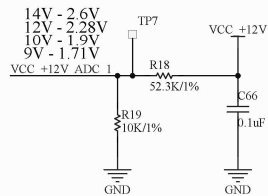
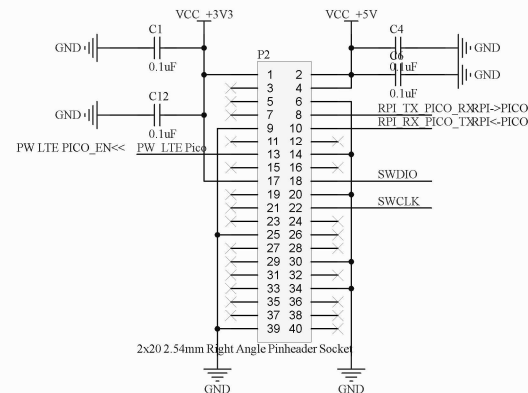
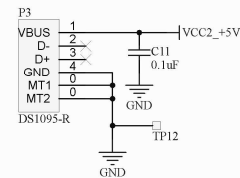


USB A , DC5V 2A



APPROVALS		DATE		PROJECT 	
ENG:	.				
DSN:	.			PROJECT REVISION: Not in version control	
CHK:	.			DOCUMENT REVISION: Not in version control	
REFERENCE DOCUMENTS				TITLE	
BOM:				*	
ASSY DWG:		SIZE	CAGE CODE	DWG NO.	
FAB DWG:		A3		REV	
PCB DWG:		SCALE:	FILE NAME	RPI HAT:SchDoc	
				SHEET	OF